## **AMENDMENTS TO THE SPECIFICATION:**

Please amend the paragraph beginning on page 1, line 11 as follows:

Host processor systems may store and retrieve data using a storage device containing a plurality of host interface units (host adapters), disk drives, and disk interface units (disk adapters). Such storage devices are provided, for example, by EMC Corporation of Hopkinton, Mass. and disclosed in U.S. Patent No. 5,206,939 to Yanai et al., U.S. Patent No. 5,778,394 to Galtzur et al., U.S. Patent No. 5,845,147 to Vishlitzky et al., and U.S. Patent No. 5,857,208 to Ofek. The host systems access the storage device through a plurality of channels provided therewith. Host systems provide data and access control information through the channels to the storage device and the storage device provides data to the host systems also through the channels. The host systems do not address the disk drives of the storage device directly, but rather, access what appears to the host systems as a plurality of logical disk units. The logical disk units may or may not correspond to the actual disk drives. Allowing multiple host systems to access the single storage device unit allows the host systems to share data stored therein.

Please amend the paragraph beginning on page 5, line 7 as follows:

According to the present invention, ordering data writes include a host computer providing a plurality of data writes to a primary storage device, the primary storage device assigning a first sequence number to data writes begun after a first time and before a second time, the primary storage device assigning a second sequence number, different from the first

sequence number, to data writes begun after the second time, in response to completion of all writes assigned the first sequence number, the primary storage device transferring writes assigned the first sequence number to a secondary storage device, and, in response to the primary storage device transferring all writes assigned the first sequence number to the secondary storage device, the primary storage device sending a commit message to the secondary storage device. Ordering data writes may also include, in response to receiving a commit message from the primary storage device, the secondary storage device storing the data writes having the first sequence number. Ordering data writes may also include, in response to storing all of the data writes having the first sequence number, the secondary storage device sending an acknowledge acknowledgement to the primary storage device. Ordering data writes may also include, following sending a commit message to the secondary storage device, the primary storage device suspending transferring data to the secondary storage device. Ordering data writes may also include, following suspending transferring data, the primary storage device assigning a third sequence number, different from the first and second sequence numbers, to subsequent data writes. Ordering data writes may also include, in response to the secondary storage device sending an acknowledge acknowledgement to the primary storage device, the primary storage device resuming transferring data to the secondary storage device. Ordering data writes may also include providing data writes to cache slots of the primary storage device. Ordering data writes may also include, in response to a data write being assigned the second sequence number corresponding to a cache slot already assigned the first sequence number, copying the data to a new cache slot. Ordering data writes may also include the primary storage device using a first list of pointers to the cache slots for data writes assigned the first sequence number and the primary storage device using a second list of pointers to the cache slots for data writes assigned

the second sequence number. Ordering data writes may also include providing a cache stamp field in a header portion of each of the slots, the cache stamp field including a sequence number associated with the slot. The cache stamp field may also include a password field that is written when the slot is first used. Ordering data writes may also include, in response to a slot no longer being used, clearing the password field. Ordering data writes may also include the primary storage device using a first cache only virtual device for the cache slots corresponding to data writes assigned the first sequence number and the primary storage device using a second cache only virtual device for the cache slots corresponding to data writes assigned the second sequence number.